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10/578,004	05/03/2006	Atsushi Ikeda	071971-0547	5214
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MCDERMOTT WILL & EMERY LLP			YEUNG LOPEZ, FEIFEI	
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)	
	10/578,004	IKEDA ET AL.	
	Examiner Feifei Yeung-Lopez	Art Unit 2826	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 03 May 2006.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-24 is/are pending in the application.
 - 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-24 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date <u>5/03/06</u> | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Specification

1. Claims 1,13, and 14 are objected to because of the following informalities:
2. Regarding claims 1 and 13, change "loose" to "lose". Appropriate correction is required.
3. Regarding claim 13, change the second "a barrier metal film" to "the barrier metal film" to properly referring back.
4. Regarding claim 14, change "a barrier metal film" to "the barrier metal film," and change "a buried interconnect" to "the buried interconnect," to properly referring back.

Claim Rejections - 35 USC § 103

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

7. Claims 1-7,9, and 12 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bernstein et al (US Patent 6,452,251 B1), in view of Zhang et al (US Patent 6,420,740 B1), further in view of Wang et al (US Patent 6,462,417 B1).

8. Regarding claim 1, Bernstein teach that a semiconductor device comprising: an insulation film (film 29 in fig. 1F) formed on a substrate (insulator 22); a buried interconnect (copper layer 53) formed in the insulation film; and a barrier metal film (layers 41, 49, 51) formed between the insulation film and the buried interconnect, wherein the barrier metal film is formed of a lamination film of a metal compound film (layer 41 in fig. 1F) and a metal film (layer 51 in fig. 1F, column 4, lines 17-21).

9. However, Bernstein do not teach that the metal film having a property of not losing its conductivity when being oxidized.

10. In the same field of endeavor, Zhang teach a metal which does not lose its conductivity when being oxidized (column 2, lines 4-5) for the benefit of having good adhesion with the substrate (column 2, lines 59-61).

11. Thus, it would have been obvious to one of ordinary skill in the art at the time of the invention to use a metal which does not lose its conductivity when being oxidized for the benefit of having good adhesion with the substrate.

12. Also, Bernstein do not teach that a fusion layer obtained through fusion of the metal compound film and the metal film with each other is present in the vicinity of an interface between the metal compound film and the metal film.

13. In the same field of endeavor, Wang teach that a fusion layer (CuTi layer 226 in fig. 3) obtained through fusion of the metal compound film (TiN barrier material interface

227 in fig. 3) and the metal film (copper seed layer 228 in fig. 3, column 5, lines 26-52) with each other is present in the vicinity of an interface between the metal compound film and the metal film for the benefit of forming a strong coherent bond (column 5, lines 47-52).

14. Thus, it would have been obvious to one of ordinary skill in the art at the time of the invention to form a fusion layer obtained through fusion of the metal compound film and the metal film with each other is present in the vicinity of an interface between the metal compound film and the metal film for the benefit of forming a strong coherent bond.

15. Regarding claim 2, Bernstein do not teach that the semiconductor device of claim 1, wherein a metal forming the metal compound film and a metal forming the metal film are different elements from each other.

16. Wang teach that a metal forming the metal compound film and a metal forming the metal film are different elements from each other (column 5, lines 5-52).

17. Regarding claim 3, Bernstein teach that the semiconductor device of claim 1, wherein a metal forming the metal compound film and a metal forming the metal film are the same element (column 4, lines 17-21).

18. Regarding claim 4, Bernstein do not teach the fusion layer includes at least several atomic layers.

19. Wang teach the fusion layer includes at least several atomic layers (the alloy-barrier material has 10 to 100 angstroms of thickness, column 5, lines 67-68).

20. Regarding claim 5, Bernstein teach the metal compound film is formed so as to be jointed with the insulation film (film 29 in fig. 1F).
21. Bernstein do not teach the metal film is formed on the metal compound film.
22. Wang teach the metal film is formed on the metal compound film.
23. Regarding claim 6, Bernstein teach that the semiconductor device of claim 1, wherein a metal forming the metal compound film is a refractory metal (column 4, lines 17-21).
24. Regarding claim 7, Bernstein teach that the semiconductor device of claim 1, wherein the metal compound film has conductivity (column 3, lines 37-38 and column 4, lines 17-21).
25. Regarding claim 9, Bernstein do not teach that the semiconductor device of claim 1, wherein the metal compound film is formed of a metal nitride film.
26. Wang teach that the metal compound film is formed of a metal nitride film (TiN barrier material, layer 227 in fig. 3, lines 26-39).
27. Regarding claim 12, Bernstein teach that the semiconductor device of claim 1, wherein the buried interconnect is formed of copper (layer 53, fig. 1F, column 4, lines 30-31).
28. Claims 13,16,18-19, and 24 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bernstein et al (US Patent 6,452,251 B1), in view of Zhang et al (US Patent 6,420,740 B1).
29. Regarding claim 13, Bernstein teach that a method for fabricating a semiconductor device, comprising the steps of: forming a recess portion in an insulation

film (insulating layer 29 in fig. 1F) provided on a substrate (insulator 22); forming a barrier metal film (layers 41,49, and 51) so that the barrier metal film covers surfaces of the recess portion; and forming a buried interconnect (copper layer 53) on the barrier metal film so that the recess portion is filled, wherein the step of forming the barrier metal film includes the step of forming a metal compound film (layer 41 in fig. 1F, column 4, lines 17-21) so that the metal compound film covers surfaces of the recess portion and then forming on the metal compound film by physical vapor deposition (column 3, lines 43-45) a metal (layer 51 in fig. 1F, column 4, lines 17-21) film.

30. However, Bernstein do not teach that the metal film not losing its conductivity when being oxidized.

31. In the same field of endeavor, Zhang teach a metal which does not lose its conductivity when being oxidized (column 2, lines 4-5) for the benefit of having good adhesion with the substrate (column 2, lines 59-61).

32. Thus, it would have been obvious to one of ordinary skill in the art at the time of the invention to use a metal which does not lose its conductivity when being oxidized for the benefit of having good adhesion with the substrate.

33. Regarding claim 16, Bernstein teach that the method of claim 13, wherein a metal forming the metal compound film and a metal forming the metal film are the same element (column 4, lines 17-21).

34. Regarding claim 18, Bernstein teach that the method of claim 13, wherein a metal forming the metal compound film is a refractory metal (column 4, lines 17-21).

35. Regarding claim 19, Bernstein teach that the method of claim 13, wherein the metal compound film has conductivity (layer 41 in fig. 1F, column 3, lines 37-39).
36. Regarding claim 24, Bernstein teach that the method of claim 13, wherein the buried interconnect is formed of copper (copper layer 53 in fig. 1F).
37. Claim 14-15,17, and 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bernstein et al (US Patent 6,452,251 B1), in view of Zhang et al (US Patent 6,420,740 B1) as applied to claim 13 above, and further in view of Wang et al (US Patent 6,462,417 B1).
38. Regarding claim 14, the previous combination remains as applied in claim 13.
39. Also, Bernstein teach the step of forming the buried interconnect (copper layer 53) that fills the recess portion (fig. 1F).
40. However, the previous combination does not teach the step of forming a seed layer on the barrier metal film.
41. In the same field of endeavor, Wang teach forming a seed layer (layer 228 in fig. 3) on the barrier metal film for the benefit of forming a strong coherent bond (column 5, lines 47-52).
42. Thus, it would have been obvious to one of ordinary skill in the art at the time of the invention to include a seed layer on the barrier metal film for the benefit of forming a strong coherent bond.
43. Regarding claim 15, the previous combination remains as applied in claim 13.

44. However, the previous combination does not teach a metal forming the metal compound film and a metal forming the metal film are different elements from each other.

45. In the same field of endeavor, Wang teach that a metal forming the metal compound film and a metal forming the metal film are different elements from each other (column 5, lines 5-52) for the benefit of forming a strong coherent bond (column 5, lines 47-52).

46. Thus, it would have been obvious to one of ordinary skill in the art at the time of the invention to include a metal forming the metal compound film and a metal forming the metal film different elements from each other for the benefit of forming a strong coherent bond (column 5, lines 47-52).

47. Regarding claim 17, the previous combination remains as applied in claim 13.

48. However, the previous combination does not teach a fusion layer.

49. In the same field of endeavor, Wang teach that a fusion layer (CuTi layer 226 in fig. 3) obtained through fusion of the metal compound film (TiN barrier material interface 227 in fig. 3) and the metal film (copper seed layer 228 in fig. 3, column 5, lines 26-52) with each other is present in the vicinity of an interface between the metal compound film and the metal film, and wherein the fusion layer includes at least several atomic layers (column 5, lines 56-59), for the benefit of forming a strong coherent bond (column 5, lines 47-52).

50. Thus, it would have been obvious to one of ordinary skill in the art at the time of the invention to form a fusion layer obtained through fusion of the metal compound film

and the metal film with each other is present in the vicinity of an interface between the metal compound film and the metal film, and wherein the fusion layer includes at least several atomic layers for the benefit of forming a strong coherent bond.

51. Regarding claim 21, the previous combination remains as applied in claim 13.
52. However, the previous combination does not teach a metal nitride film.
53. In the same field of endeavor, Wang teach a metal compound film is formed of a metal nitride film (TiN barrier material, layer 227 in fig. 3, lines 26-39).
54. Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Bernstein et al (US Patent 6,452,251 B1), in view of Zhang et al (US Patent 6,420,740 B1), further in view of Wang et al (US Patent 6,462,417 B1) as applied to claim 1 above, and still further in view of Asahina et al (US Patent 6,144,097).
55. Regarding claim 8, the previous combination remains as applied in claim 1.
56. However, the previous combination does not teach the metal compound film formed of a metal oxide film.
57. In the same field of endeavor, Asahina teach that a metal compound film is formed of a metal oxide film for the benefit of improving the barrier properties (column 1, lines 40-44).
58. Thus, it would have been obvious to one of ordinary skill in the art at the time of the invention to include the metal compound film formed of a metal oxide film for the benefit of improving the barrier properties.
59. Claim 20 is rejected under 35 U.S.C. 103(a) as being unpatentable over Bernstein et al (US Patent 6,452,251 B1), in view of Zhang et al (US Patent 6,420,740

B1) as applied to claim 13 above, and further in view of Asahina et al (US Patent 6,144,097).

60. Regarding claim 20, the previous combination remains as applied in claim 13.

61. However, the previous combination does not teach the metal compound film formed of a metal oxide film.

62. In the same field of endeavor, Asahina teach that a metal compound film is formed of a metal oxide film for the benefit of improving the barrier properties (column 1, lines 40-44).

63. Thus, it would have been obvious to one of ordinary skill in the art at the time of the invention to include the metal compound film formed of a metal oxide film for the benefit of improving the barrier properties.

64. Claim 10 is rejected under 35 U.S.C. 103(a) as being unpatentable over Bernstein et al (US Patent 6,452,251 B1), in view of Zhang et al (US Patent 6,420,740 B1), further in view of Wang et al (US Patent 6,462,417 B1) as applied to claim 1 above, and further in view of Wang et al (US Patent 6,445,070 B1).

65. Regarding claim 10, the previous combination remains as applied in claim 1.

66. However, the previous combination does not teach the metal compound film formed of a metal carbide film.

67. In the same field of endeavor, Wang teach a metal compound film formed of a metal carbide film for the benefit of forming a strong coherent bond (column 5, lines 28-52).

68. Thus, it would have been obvious to one of ordinary skill in the art at the time of the invention to include a metal compound film formed of a metal carbide film for the benefit of forming a strong coherent bond.

69. Claim 22 is rejected under 35 U.S.C. 103(a) as being unpatentable over Bernstein et al (US Patent 6,452,251 B1), in view of Zhang et al (US Patent 6,420,740 B1) as applied to claim 13 above, and further in view of Wang et al (US Patent 6,445,070 B1).

70. Regarding claim 22, the previous combination remains as applied in claim 13.

71. However, the previous combination does not teach the metal compound film formed of a metal carbide film.

72. In the same field of endeavor, Wang teach a metal compound film formed of a metal carbide film for the benefit of forming a strong coherent bond (column 5, lines 28-52).

73. Thus, it would have been obvious to one of ordinary skill in the art at the time of the invention to include a metal compound film formed of a metal carbide film for the benefit of forming a strong coherent bond.

74. Claim 11 is rejected under 35 U.S.C. 103(a) as being unpatentable over Bernstein et al (US Patent 6,452,251 B1), in view of Zhang et al (US Patent 6,420,740 B1), further in view of Wang et al (US Patent 6,462,417 B1) as applied to claim 1 above, and further in view of Terry et al (US Patent 4,675,713).

75. Regarding claim 11, the previous combination remains as applied in claim 1.

76. However, the previous combination does not teach the metal compound film formed of a metal silicide film.

77. In the same field of endeavor, Terry teach a metal compound film formed of silicide for the benefit of improving reliability (abstract).

78. Thus, it would have been obvious to one of ordinary skill in the art at the time of the invention to include a metal compound film formed of silicide for the benefit of improving reliability.

79. Claim 23 is rejected under 35 U.S.C. 103(a) as being unpatentable over Bernstein et al (US Patent 6,452,251 B1), in view of Zhang et al (US Patent 6,420,740 B1) as applied to claim 13 above, and further in view of Terry et al (US Patent 4,675,713).

80. Regarding claim 23, the previous combination remains as applied in claim 13.

81. However, the previous combination does not teach the metal compound film formed of a metal silicide film.

82. In the same field of endeavor, Terry teach a metal compound film formed of silicide for the benefit of improving reliability (abstract).

83. Thus, it would have been obvious to one of ordinary skill in the art at the time of the invention to include a metal compound film formed of silicide for the benefit of improving reliability.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Feifei Yeung-Lopez whose telephone number is 571-

270-1882. The examiner can normally be reached on 7:30am-5:00pm Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sue Purvis can be reached on 571-272-1236. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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